Hardware Security Workshop Schedule

Date: July 27, 2019
Venue: A-007, R&D Block, IIIT Delhi

Day and Date	Time	Topic
Saturday July 27, 2019	8 am – 8.30 am	Registration
	8.30 am – 8.45 am	Inauguration
	9.00 am – 10.30 am	Speaker: Prof. Prabhat Mishra (Talk over Skype) Title: Securing Hardware for Designing Trustworthy Systems
	10.30 am – 11.00 am	Tea and Snacks
	11.00 am – 12.30 pm	Speaker: Dr. Pramod Subramanyan Title: End-to-end Verification of System-level Security Properties in SoC Designs
	12.30 pm – 1.30 pm	Lunch
	1.00 pm – 2.00 pm	Poster Session
	2.00 pm – 3.00 pm	Speaker: Dr. Ashish Kumar Title: Circuits and Architecture for Safety-Critical Embedded Memories
	3.00 pm – 3.30 pm	Tea and Snacks
	3.30 pm – 4.30 pm	Speaker: Mr. Vineet Dwivedi Title: Hardware Security and Testing
	4.30 pm – 5.00 pm	Award and Thanks Giving