INDRAPRASTHA INSTITUTE *of* INFORMATION TECHNOLOGY DELHI

SPARC
Scheme for Promotion of Academic and Research Collaboration

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

One Day Workshop
on
Hardware Security
July 27, 2019 (Saturday)

Organized By
IIIT Delhi

Sponsored by
Scheme for Promotion of Academic and Research Collaboration (SPARC)

Invited Speakers

Prof. Prabhat Mishra
Professor
University of Florida, USA

Dr. Pramod Subramanyan
Assistant Professor
IIT Kanpur, India

Dr. Ashish Kumar
Group Manager
STMicroelectronics, India

Mr. Vineet Dwivedi
Scientist G
SAG, DRDO, India

Workshop Coordinator

Dr. Sujay Deb, IIIT Delhi
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Special Acknowledgement

Scheme for Promotion of Academic and Research Collaboration (SPARC)

POSTER COMPETITION
on Hardware Security
Prize money worth 30K

About the Workshop

The workshop is a whole day informative program providing an overall picture of current trends and advancements in hardware security. Security and trust are paramount considerations while designing embedded systems. Majority of the embedded systems are designed using both System-on-Chip (SoC) components and software applications. While significant efforts have been made on software security for multiple decades, hardware security has got its due attention for the last few years. The objective of the workshop is to generate awareness about design aspects for hardware security among system designers, researchers, and students through interactive sessions. The whole day program will cover a wide variety of solutions related to design-for-security.

Key Goals

➢ Participants will learn about wide variety of hardware security vulnerabilities, design-for-security solutions, and possible attacks and countermeasures.

➢ To discuss the challenges associated with future hardware security, safety critical embedded memories, integrity of third-party IPs, security verification etc.

➢ To provide a forum for researchers to present and discuss innovative ideas and solutions pertaining to hardware security.

Workshop Organization

The workshop would provide a broad picture of challenges and solution in the hardware security field such as security and trust verification, designing SoCs using third party IPs, trojans in SoCs, side-channel-analysis, safety and security implementations in embedded memories, security and testing etc. There will be two sessions by distinguished academicians and two sessions by eminent industry personnel.

Target Audience

• B.Tech students having interest in this field
• M.Tech, Ph.D & Research scholars
• Faculty members and Professionals from Industry

Poster Competition

• Poster presentation session on Hardware Security is open to all
• Prize money worth 30K
• Last date of abstract submission July 20, 2019

Hosted By

Department of ECE, IIIT-Delhi, Okhla Industrial Phase III (Near Govindpuri Metro) New Delhi, India – 110020

Contact Details:

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Registration Details:

Registration Fees:

Students (including Ph.D & research scholars) - INR 500

Professionals - INR 1000

Registration Deadline: July 20,2019 (extended)

Registration Link: https://www.iiitd.edu.in/noc/hsw/