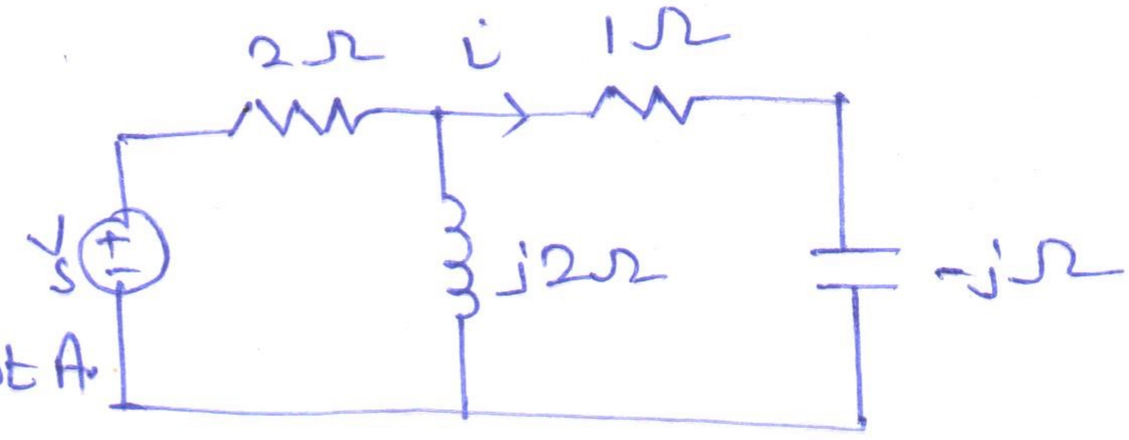
HA#4

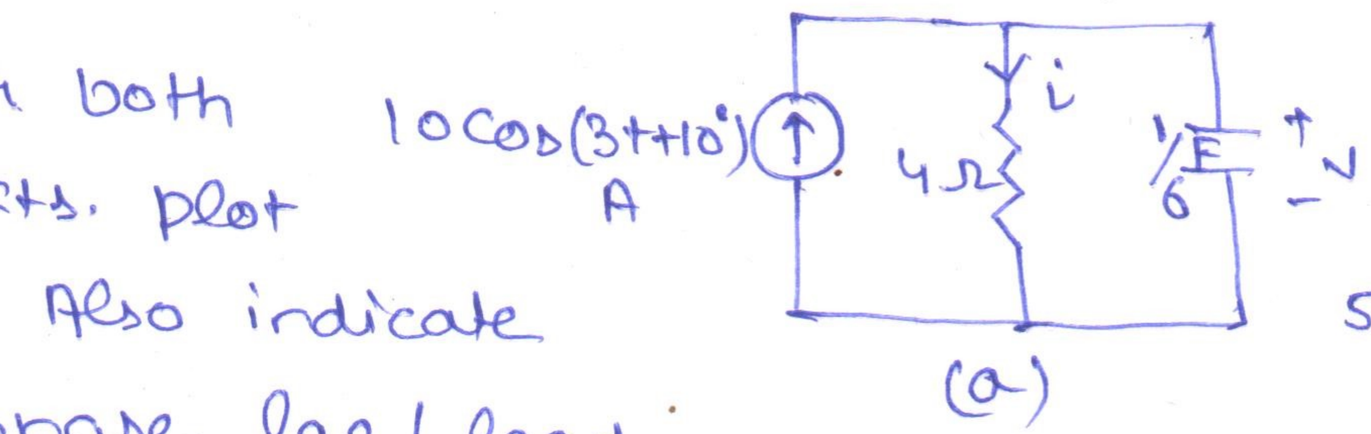
Q!- For this ckt. find & plot V_s when 'i' is given as $0.5 \sin 200t$ A.

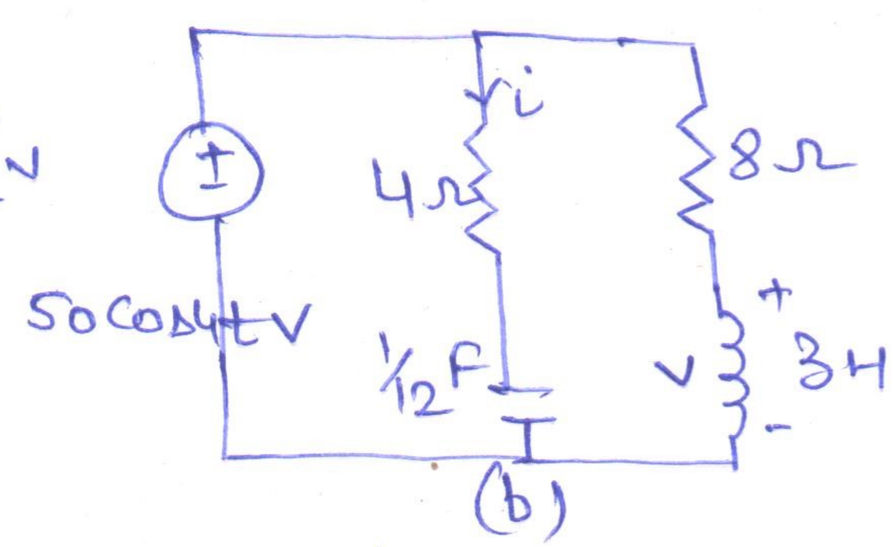


Indicate the phase lag / lead of voltage source w.r.t current, i. Also give reasoning. (10)

Q:-

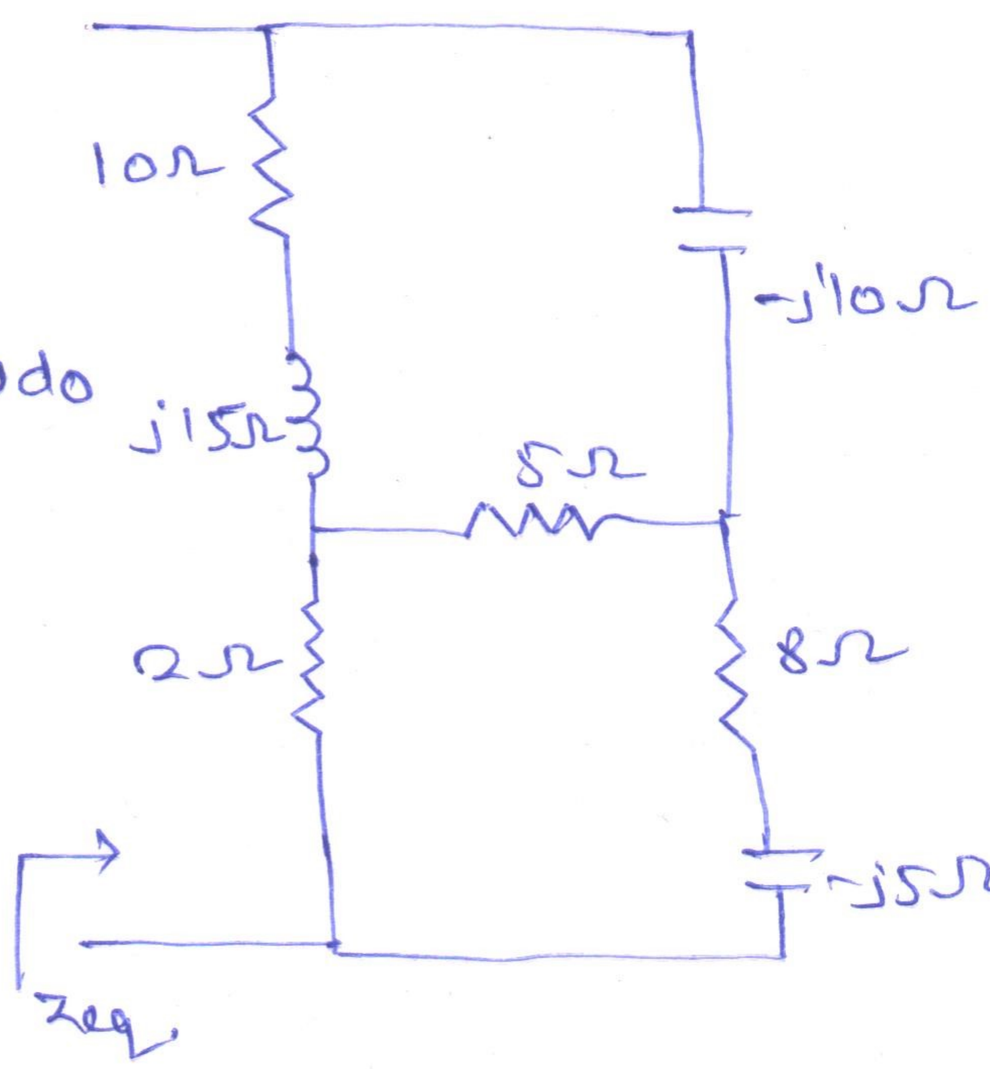
For both the ckts. plot i & v. Also indicate the phase lag / lead.





What inferences you conclude from the results? (10)

Q!- For the ckt. deduce the net impedance, Z_{eq}. Now, suppose a pseudo voltage source drives the ckt. The angular frequency of the voltage source is 100 rad/s.



Plot current through -j10Ω, j15Ω and voltage across -j15Ω. Indicate the phase lag / lead.